



RE-EXAMINATION TIMETABLE, MARCH, 2026
BACHELOR OF TECHNOLOGY (B.Tech.)

THIRD YEAR, SEMESTER-V (for 2022-23 to 2023-24 Batches Under Autonomy)

Timings: 10:00 a.m. to 01:00 p.m.

Date: 20th February, 2026

Day & Date	Mechanical Engineering	Electronics & Telecommunication Engineering	Computer Science & Engineering (Data Science)
Tuesday, 17/03/2026	-	Radio Frequency Circuit Design	Machine Learning-II (Deep Learning)
Friday, 20/03/2026	Mechanical Vibrations	Digital Signal Processing	-
Tuesday, 24/03/2026	Heat Transfer	Data Structures & Algorithms	-
Friday, 27/03/2026	-	Microprocessor & Microcontroller	-
Monday, 30/03/2026	Automotive Prime Movers	Neural Network & Fuzzy Logic	-

Important Note: Change if any, in the time table shall be communicated on the College website (www.djsce.ac.in).

Ms. Alka Shukla
Controller of Examinations

Dr. Hari Vasudevan
Principal